

# Investigating the effect of thermal stresses on Multijunction PV solar cells

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**Abstract.** Multijunction Photovoltaics (MJPV) technologies represent a field of robust research aimed at enhancing photovoltaic efficiencies. Perovskite, GaAs, and GaN semiconductors with organic substrates are suitable for multi-junction solar cells due to their ease, efficiency, and cost-effectiveness in deposition on the active layers of silicon solar cells. Thermal stresses arise at the manufacturing and operational temperatures of Si/indium/perovskite, Si/indium/GaN, and Si/indium/GaAs multijunction photovoltaic cells due to thermal mismatch. This work aims to evaluate the effects of thermal loading on a multi-junction system composed of perovskite, GaAs, and GaN particles on silicon, utilising finite element analysis software (ABAQUS) and representative volume elements (RVE). One method experienced reduced stress compared to the other because of the differing material properties. Moreover, the GaAs region of model (C) experiences the highest level of stress. Quantitative analysis indicated that the Model C, that includes GaAs as upper layer, exhibited the highest thermal stress concentration at 48MPa, notably exceeding the other models; for instance, Model B with GaN recorded a peak stress of 45.5 MPa, while Model A displayed a maximum stress of 41 MPa in the indium section. These findings highlight the influence of material characteristics on stress distribution in multijunction solar cells. Future research will examine the impact of different thermal configurations relevant to the operational conditions of solar panels and utilise the Embedded Void Approach (EVA) to mitigate dislocation propagation induced by elevated thermal stresses.

## 1. Introduction

Over the last few decades, silicon has shown to be exceptionally reliable and meets the lifetime requirements of most photovoltaic solar cell applications. Given that silicon is an abundant element on Earth, there is enough supply of raw materials for its extraction. Moreover, the wavelength range of the crystalline silicon solar cell varies from 300 to 1200 nm, and the efficiency for the crystalline silicon solar cell can reach 25.6%, with a practical limit of 27% [1]. Another technology of the solar cell is the single-junction solar cell, and this type is getting closer to the potential maximum efficiency. On the other hand, solar cells employ the GaAs semiconductor material, which presently boasts the highest efficiency in single-junction technology at 28.8% [2]. The theoretical efficiency limit (radiative limit) for the GaAs solar cell can reach approximately 32.5% [3].

The aforementioned information for the single-junction solar cells highlights that its efficiency is limited, and it is essential to enhance the solar cell's effectiveness and reduce the thickness of the

absorption layer in order to make it more economically viable. Therefore, to overcome this issue, researchers introduced a new technology for solar cells, which is the multijunction solar cell. This technology uses a few different absorber materials to collect a broader range of the solar spectrum and is one way to increase the fundamental c-Si cell efficiency constraint. A wider bandgap material, on the other hand, minimises heat carrier losses and boosts solar cell efficiency. Under conventional test settings, in 2017, the potential conversion efficiency for the MJ-PV reached 46% [8][4]. There have been several different tandem solar cell technologies created so far [5–10], and [10]. Furthermore, Tandem cells can achieve efficiencies higher than 50%, which is substantial improvement over single-junction silicon cells. With a perovskite/silicon arrangement, the record efficiency for a tandem solar cell as of 2022 is 29.15% [11]. Meanwhile, the III-V semiconductors, such as InGaAs, AlInP, AlGaInP, GaInP, InP, GaAs, etc., have garnered considerable attention in solar cell technology, especially with MJPV cells, as using III-V semiconductors can enhance the power conversion efficiency (PCE) of more than 40%, surpassing that of other types of solar cells; however, implementing the MJ-PV cells for a large-scale power plant is a hurdle, as the cost will be very high [11],[12].

Over the preceding two decades, researchers have been trying to find an affordable and effective semiconductor material and enhancing the design for MJPV cells to be more efficient. Additionally, subsequent advantages of perovskite ( $\text{CH}_3\text{NH}_3\text{PbI}_3$ ) may elucidate its remarkable photovoltaic characteristics: extended diffusion lengths for charge carriers, non-polar mobility for photon-induced carriers, abundant compositions, and adjustable band gaps ranging from 1.2 to 3.1 eV. Perovskite solar cells (PSC) have been recognised as an appropriate upper sub-cell for tandem or multi-junction configurations due to their tunable band gap, their shorter wavelength spectrum (300–800 nm), and optoelectronic properties, among other aforementioned characteristics. Moreover, the overall photoelectric conversion efficiency of perovskite solar cells (PSC) is 21% [13,14]. Furthermore, GaN material, which is commonly used in power amplifiers (PAs) for terrestrial applications, has attracted interest for space applications due to its remarkably high power levels at pertinent frequencies, coupled with minimal losses in passive networks and suboptimal noise performance in receivers [15]. Some recent studies are focused on finding a method to minimise dislocation and other faults in multijunction PV cells that happen as a result of thermal stresses [16–18]. In those studies, ABAQUS software was used to examine the impact of voids on the dislocation propagation and the stresses, and the displacement was used as an indication for the dislocation density. Furthermore, the involvement of the voids in close proximity to the GaAs/Si multijunction solar cell shows a decrease in thermal stresses, which consequently reduces the dislocations. The result shows that the stress in the Si layer reduces by 14% and increases 18% in the GaAs layer, as well as the maximum displacement dropping to 11%. Overall, this approach is a suitable solution for the 1.0 eV sub-cell, which could reach the efficiency of 45% [18].

Moreover, Bagheri et al. [19] conducted a simulation for InGaP/GaAs MJPV with AlGaAs tunnel junction in order to investigate the power conversion efficiency of the MJPV cells by manipulating the window layers and introducing back-surface field layers. The findings show that MJ-PV cells with six tunnel layers of different thicknesses can reach a high PCE of 35.5% when InAlGaP is used in the back-surface field and window layer. In addition, the PCE was improved to 36.24% by increasing the thickness of the rear surface field layer of the top cell.

As previously stated, MJPV cells have the ability to exceed silicon single-junction solar cells' intrinsic efficiency limit. However, silicon can still be useful as a bottom junction and substrate material in these solar cells. This feature is alluring because the majority of thin-film absorbers necessitate a support structure due to their inadequate thickness for independent stability. Silicon possessing a bandgap of 1.1 eV is highly appropriate for dual-junction and triple-junction devices. The ideal bandgap energy for adding an additional absorber atop silicon is 1.7 eV; for two absorbers in series, it is 2.0 eV, and for two absorbers in parallel, it is 1.5 eV. The exact bandgaps depend on several factors, including the long-wavelength response of the silicon-bottom solar cell and the transparency of the upper layers. This seems to be a substantial challenge in the fabrication of tandem solar cells on

silicon. To facilitate the deposition of planar thin-film absorbers on the front, the light-trapping features must be repositioned from the front side to the rear of the wafer [20–22]. One method to accomplish this is by integrating a pyramid texture, spheres, or nanostructured grating on the posterior surface of the silicon wafer. This will effectively extend the light path through the silicon, leading to enhanced absorption at the indirect bandgap [23–25].

Lastly, Soma and Mohammad [26] used the finite element method (FEM) to simulate a three-dimensional planar perovskite solar cell. The suggested device is comprehensively described with an optical-electrical hybrid modelling technique. To validate the proposed version, it is essential to compare it with experimental results. The incorporation of a secondary absorber layer made of  $\text{CH}_3\text{NH}_3\text{SnI}_3$  improves the architecture of a perovskite solar cell that depends exclusively on  $\text{CH}_3\text{NH}_3\text{PbI}_3$ . Despite the  $\text{CH}_3\text{NH}_3\text{SnI}_3$  layer being roughly 200 nanometres in thickness, the recent collaboration has elevated the PCE from approximately 14.32 percent to around 15.32 percent. Applying a 40 nm  $\text{MgF}_2$  coating, which serves as an anti-reflective agent, to a surface with a designated percentage can boost the short circuit current ( $J_{sc}$ ). Upon evaluating the findings of the anti-reflectors, the succeeding allocation will be regularly modified in the future. Substituting the flat back layer with a regularly corrugated decreasing back layer and the anti-reflector with a dynamic layer might yield a PCE of 17.5%, representing the proposed photovoltaic cell configuration (PCC) for this innovation.

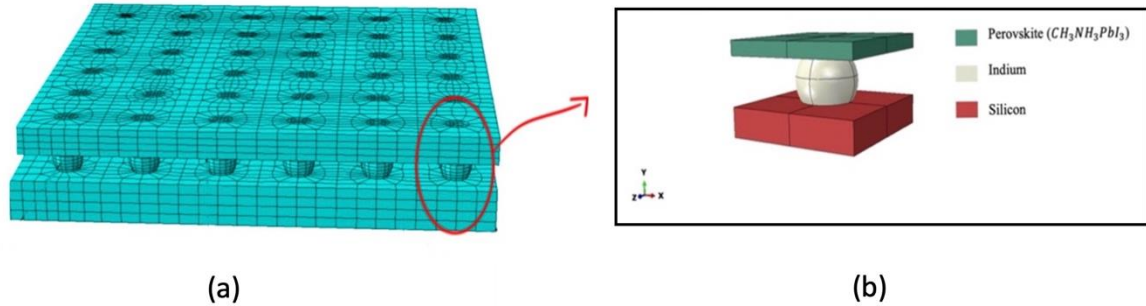
The aforementioned studies highlighted that the use of MJPV cells combined with GaAs or  $\text{CH}_3\text{NH}_3\text{PbI}_3$  with a silicon substrate has the potential to increase the solar cell efficiency and can be used in space applications. Still, at the same time, there is a probability that a thermal stress occurs in those cells, which is a critical issue, as it causes dislocation in the cells that may propagate and cause cracks in the cells. In addition, a study found that the efficiency of GaN solar cells can reach 20% [27]. Therefore, investigating and understanding the impact of thermal stress on these cells is essential in order to enhance their thermal stability. The thermal stress can also cause dislocations in the cells, which will spread and lead to a crack in the cell. This will make the MJPV cell even less long-lasting and effective [27].

Although the potential efficiency for the MJPV cells is higher than the conventional solar still, it is still challenging to reach the thermal stability for the MJPV cell under different environmental conditions. Moreover, the previous studies didn't focus on investigating the effect of different operating temperatures on MJPV, especially for materials with a high potential efficiency such as GaN, perovskite, and GaAs. So, the aim of this paper is to look into how the operating temperature affects the distribution of thermal stress across the MJPV cell structure and at the interface between the layers. This will be done using finite element analysis (FEM) and thermoelasticity-based constitutive equations. Furthermore, this study will provide a foundation for further elastic/plastic simulation of MJPV cells and investigate the potential of organic-based perovskite, GaN, and GaAs as a top cell, with indium as the middle cell and silicon as a substrate. This will be integrated with the finite element method (FEM) to model a periodic representative volume element (RVE) for Si/Indium/perovskite, Si/Indium/GaN, and Si/Indium/GaAs multijunction photovoltaic cells. This study will utilise finite-element software (ABAQUS) to analyse the outcomes of these models.

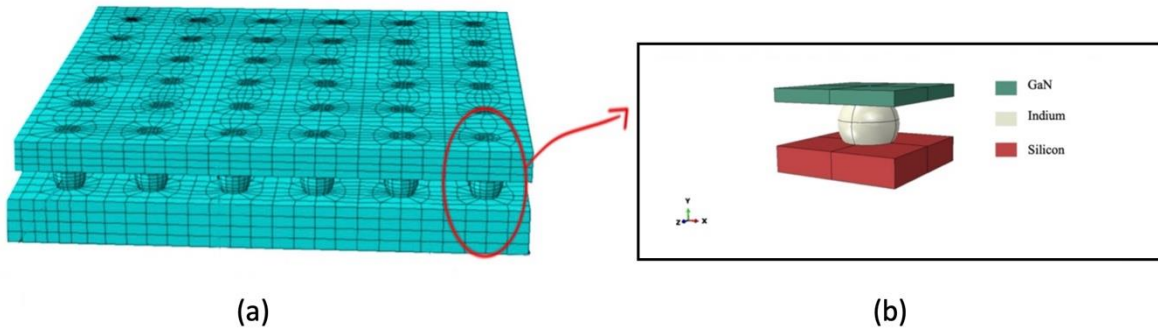
## 2. Theoretical Model

Three models have been created, all with identical geometry and materials, except for the top layer, which varied in material composition across each model. Figure 1 shows the structure of Model (A), which consisted of a silicon base layer, an indium mid-sphere, and an upper layer of inorganic-organic perovskite ( $\text{CH}_3\text{NH}_3\text{PbI}_3$ ). Similarly, the model (B) had the same structure and materials, but the upper layer was made of GaN, as shown in figure 2. Likewise, model (C) had the same structure and materials, but the upper layer was made of GaAs, as shown in figure 3. In addition, the silicon layer dimensions  $50 \times 50 \times 10 \mu\text{m}$ , the indium sphere has a radius of  $12 \mu\text{m}$ , and the upper layer ( $\text{CH}_3\text{NH}_3\text{PbI}_3$ , GaAs, GaN) dimensions  $50 \times 50 \times 5 \mu\text{m}$ . These dimensions are chosen to represent the RVE of the entire structure, as shown in figures 1-3. Furthermore, the indium sphere was located at a

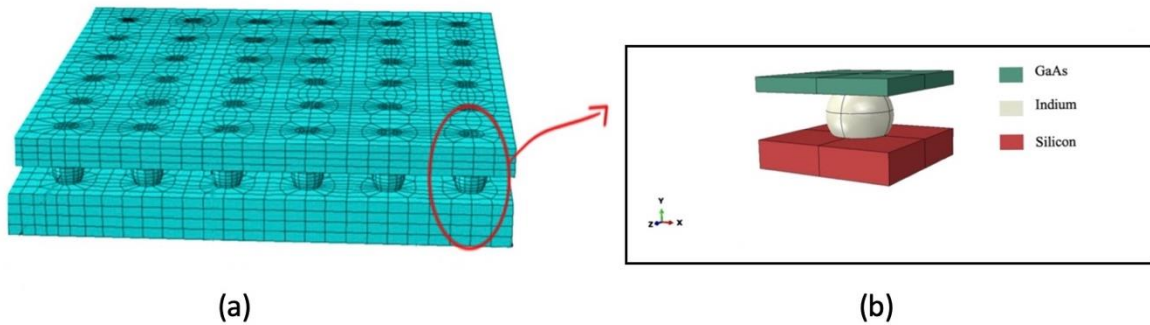
depth of  $5\ \mu\text{m}$  within the silicon layer, while the  $\text{CH}_3\text{NH}_3\text{PbI}_3$ , GaAs and GaN layers had a depth of  $2.5\ \mu\text{m}$ .



**Figure 1.** Geometry and the MJPV cell; (a) represents MJPV cell for model (A); (b) represents the RVE for model (A).

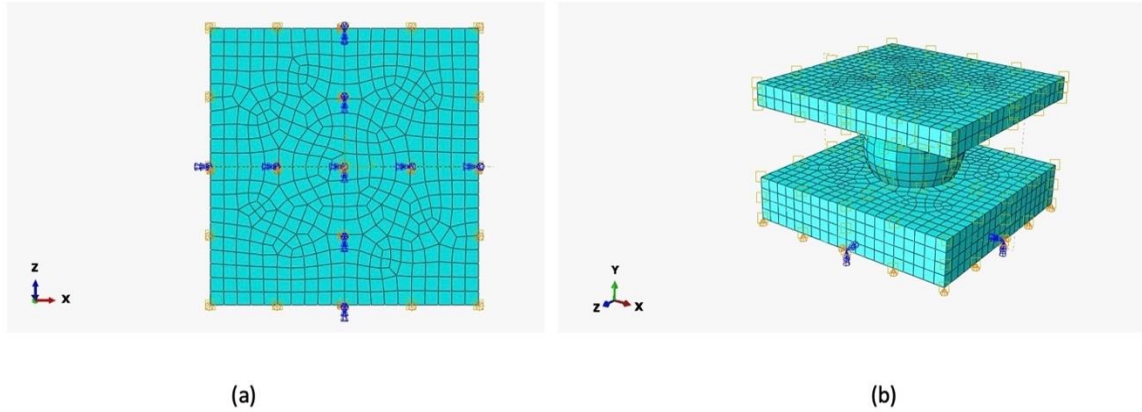


**Figure 2.** Geometry and the MJPV cell; (a) represents MJPV cell for model (B); (b) represents the RVE for model (B).



**Figure 3.** Geometry and the MJPV cell; (a) represents MJPV cell for model (C); (b) represents the RVE for model (C).

The following figure 4 shows the boundary conditions applied at the nodes located at the outer edge of the 3D structure. Moreover, symmetric planes in X and Z axis are used to apply symmetric boundary conditions to the models and the Hexahedral meshing type used for the 3D structure for more accuracy with meshing size 2.5.



**Figure 4.** Boundary conditions for the three models:(a) bottom view for the RVE; (b) isometric view for the RVE.

The material deformation can be known from the stress  $\sigma$ , strain  $\varepsilon$ , homologous temperature  $T_h$  ( $T_h = T/T_m$ ) in kelvin unit, and the microstructure (such as the grain size, impurities) of the material. Usually, the material react under specific conditions could be detect via the flow law associated with more than one mechanism. The Duhamel-Neumann thermoelastic constitutive law applied to the 3D structure that account the thermal stresses, to check the model behaviour under different temperatures [28].

$$\sigma_{ij} = C_{ijkl}\varepsilon_{kl} - \beta_{ij}(T - T_0) \quad (1)$$

Where:

- $\sigma$ : the second order Cauchy stress tensor.
- $\varepsilon$ : the second order linearized strain tensor.
- $\beta$ : the second order thermoelastic moduli tensor.
- $C$ : the fourth order elasticity tensor.
- $T$ : the current temperature.
- $T_0$ : the reference temperature.

The second step after running the thermoelastic model is to apply uniform temperature to 3D structure in order to simulate the thermal stresses happened during the fabrication and operation process of the perovskite or GaAs or GaN/Silicon. The model's initial temperature is set at 423 K, subsequently cooled to room temperature at 300 K, with mid-heating and final heating temperatures of 330 K and 358 K, respectively, applied in the simulation[29,30].

As aforementioned that the substrate layer in model A, B, C was made from silicon which is an isotropic material and the same behaviour for the GaAs, GaN, and indium. Given that the indium junction or sphere exhibits greater ductility than the silicon layer, it is predicted that the materials employed in multijunction photovoltaic cells (MJPVs) maintain elasticity during the entire process. Additionally, for each of the aforementioned materials, Poisson's ratio, Young's modulus, and the thermal expansion coefficient are considered, as the material behaviour is isotropic. Moreover, when crystalline silicon exhibiting isotropic behaviour is applied to the generic thermoelastic model, it simplifies to the following equation:

$$\sigma_{ij} = \lambda \varepsilon_{kk} \delta_{ij} + 2\mu \varepsilon_{ij} - (3\lambda + 2\mu)\alpha(T - T_0)\delta_{ij} \quad (2)$$

Where:

- $\lambda$ : Lamé's constant.
- $\mu$ : the shear modulus.
- $\delta_{ij}$ : the Kronecker delta (replacement operator).
- $\alpha$ : the coefficient of linear thermal expansion.

The fourth order elastic tensor of  $CH_3NH_3PbI_3$  crystals with independent elastic coefficients and tetragonal symmetry below 330 K is [31]:

$$[C] = \begin{pmatrix} 20.1 & 14.6 & 6.8 & 0 & 0 & 0 \\ & 20.1 & 6.8 & 0 & 0 & 0 \\ & & 17.9 & 0 & 0 & 0 \\ & & & 1.6 & 0 & 0 \\ & \text{Sym} & & & 1.6 & 0 \\ & & & & & 9.2 \end{pmatrix} \text{ (GPa)} \quad (3)$$

Both the Silicon and perovskite GaAs, GaN layers are assumed to have isotropic thermal effects. As a result,  $\beta_{ij}$  is reduced to the isotropic tensor of second order ( $\alpha_{ij}$ ) and becomes [29]:

$$\alpha_{ij} = \alpha(T - T_0)\delta_{ij} \quad (4)$$

As a result, the second order thermal strain tensor ( $\varepsilon_{ij}^T$ ) is:

$$\varepsilon_{ij}^T = \alpha_{ij}(T - T_0) \quad (5)$$

Meanwhile, the following equations are used to calculate the Von Mises stress ( $\sigma^e$ ) generated on the structure:

$$\sigma^e = \frac{3}{\sqrt{2}} \tau^{Oct} \quad (6)$$

$$\tau^{Oct} = \frac{1}{3} \sqrt{(2 I_1^2 - 6 I_2)} \quad (7)$$

$$I_1 = tr(\sigma) \quad (8)$$

$$I_2 = \frac{1}{2} (I_1^2 - tr(\sigma^2)) \quad (9)$$

Where:

- $\tau^{Oct}$ : the shear stress on the octahedral plane.
- $I_1, I_2$ : the Cauchy stress tensor invariants.

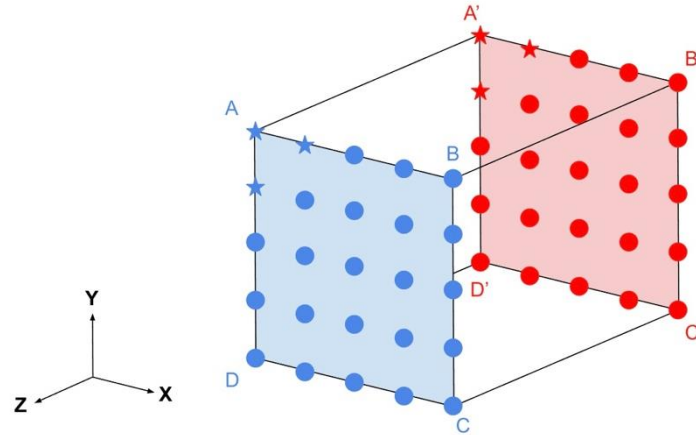
Furthermore, the following table 1 illustrate the parameter used in the software for each material.

**Table 1.** Constants Parameter for Each Material [18,29–31][18,32–35].

Parameters	Unit	$CH_3 NH_3 PbI_3$	Silicon	Indium	GaAs	GaN
Young's modulus (E)	GPa	-	168.9	14	144	330
Poisson's ratio ( $\nu$ )	-	-	0.262	0.46	0.31	0.23
$\alpha$ (k-1) x10-6	k-1	43	2.6	33	5.8	3.17

Usually the Periodic Boundary Conditions (PBCs) are a group of boundary condition that apply to Representative Volume Element (RVE) or a Statistical Volume Element (SVE), in order to simulate a large (infinite) system. However, the PBCs are not applied to the models that has a torsion and bending loads. Therefore, in this present work Extended Periodic Boundary Conditions (xPBCs) are applied to the models in order to overcome the limitation existing in the Periodic boundary conditions such as the bending and torsion load that already exist in the present 3D structure. The effect of curvature, such as in bending, cannot be accurately represented by employing a constant average strain. The strain along the axis connecting the master and slave surfaces is graphed versus the other two axes, and this constraint is alleviated to render the plot planar. This ensures periodicity while being more comprehensive, as the xPBCs equations are fulfilled by the solution to the traditional PBCs equations. The illustration of node pairing in the xPBCs is presented in figure 5. The PBCs

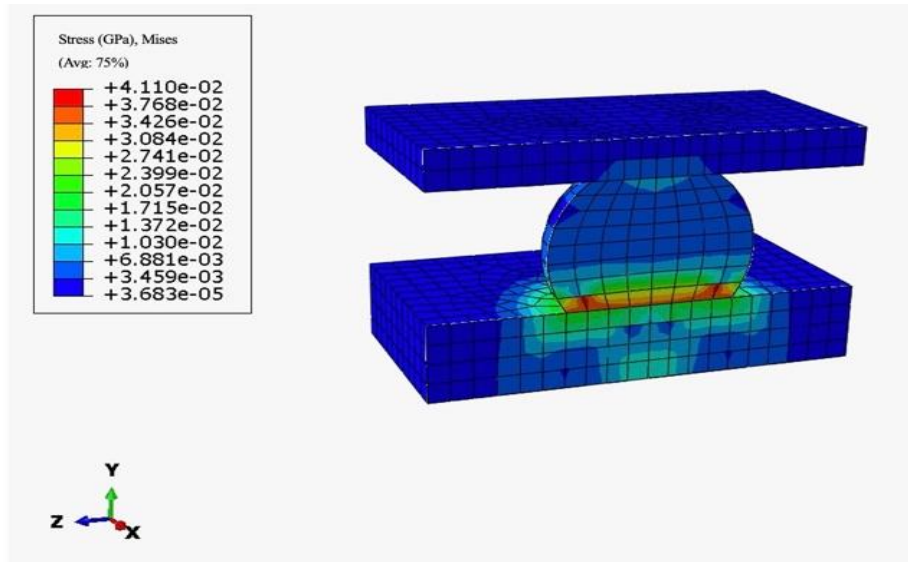
equations are utilised between any two pairings in conventional PBCs, associating each node on the master surface (ABCD) with its equivalent node on the slave surface (A'B'C'D'). To avert the occurrence of four initially collinear nodes and to generate a straightforward set of equations, xPBCs confines the first three node pairs to possess star-shaped configurations [36].



**Figure 5.** Example of node pairing for xPBCs, with blue designating the master surface and red the slave surface [36]

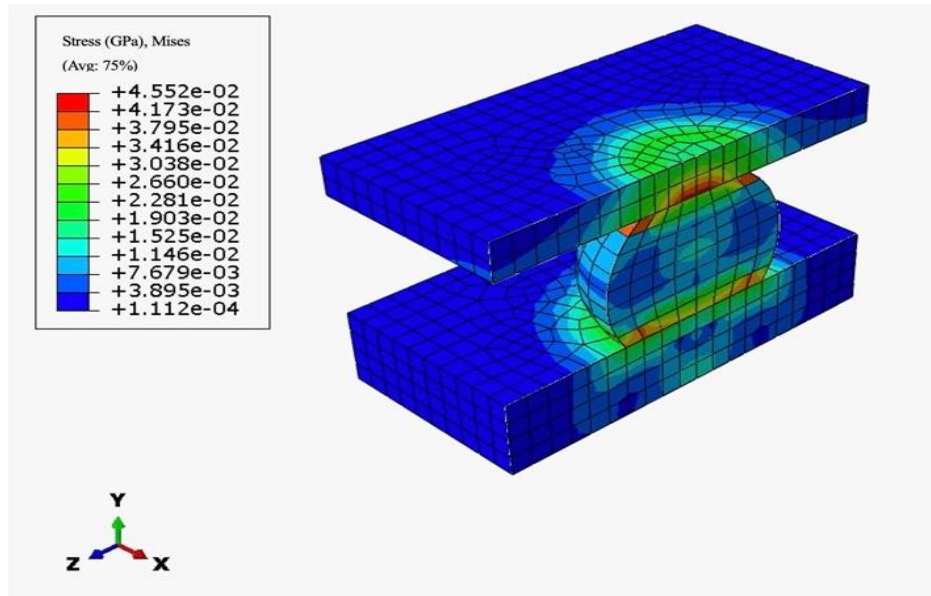
### 3. Results

A cross-section of three different models demonstrating the internal and external stress distribution. The indium segment exhibits the maximum stress concentration (average stress of 41 MPa), followed by silicon with lower stress levels (average stress of 27.4 MPa), while the perovskite section demonstrates the least stress accumulation (average stress of 3.4 MPa), as illustrated in figure 6. The indium junction/sphere undergoes slight distortion from the sides due to the considerable temperature disparity between the indium and silicon.



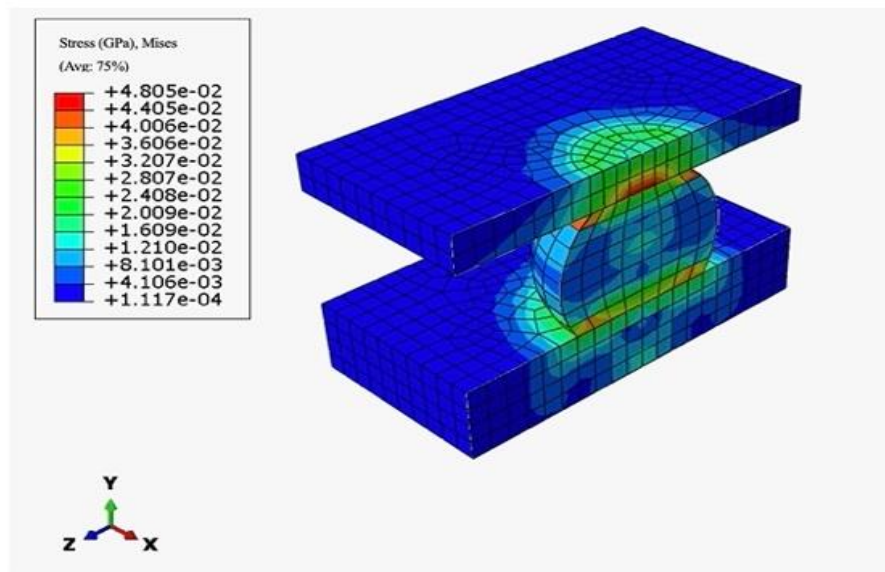
**Figure 6.** The thermal stress distribution on model (A).

Figure 7 illustrates the average stress for model B, revealing that the GaN region exhibits the largest stress concentration (average stress of 45.5 MPa), but the thermal stresses in the indium and silicon portions are equivalent to those in model A.



**Figure 7.** The thermal stress distribution on model (B).

Figure 8 illustrates that the average stress for model C indicating that the GaAs section exhibits the highest stress concentration (average stress of 48 MPa), while the thermal stress in the indium section rises to 44.5 MPa, and the average stress for silicon experiences a slight increase to 28 MPa. The cause of the increased stress is the significant temperature mismatch between GaAs and silicon.



**Figure 8.** The thermal stress distribution on model (C).

Table 2 illustrates a quantitative comparison of thermal stress between the proposed Multijunction Photovoltaic Cells (MJPVs) models and previous studies, but across different MJPV configurations. It is noteworthy that the use of an embedded void approach (EVA) in MJPVs might yield dual effects either increasing or decreasing thermal stresses, dependent upon the density and positioning of the voids. Furthermore, the thermal stress values for the proposed model differ from those in prior studies, as the proposed model incorporates three junctions and includes distinct interface zones. For instance, the current study examines the interfaces between indium/Si, indium/GaAs, indium/GaN, and indium/perovskite, whereas previous studies focused on the interfaces of GaN/Si or GaAs/Si.

**Table 2-comparison analysis of thermal stress in Multijunction Photovoltaic Cells (MJPVs) with and without Embedded Void Approach (EVA).**

System	Tool	Result	Exp./Num.	Ref.
GaN/Si-MJPVs with EVA	ABAQUS	Stress: 653 MPa	Numerical	[37]
GaN/Si-MJPVs without EVA	ABAQUS	Stress: 1.4 GPa	Numerical	[37]
GaN/Si-MJPVs without EVA	ABAQUS	Stress: 1.4 GPa	Experimental	[38]
GaAs/Si-MJPVs with EVA	ABAQUS	Si layer: 79.5 MPa, GaAs layer: 186 MPa	Numerical	[18]
GaAs/Si-MJPVs without EVA	ABAQUS	Si layer: 108 MPa, GaAs layer: 173 MPa	Numerical	[18]
Model A (Current Study)	ABAQUS	Indium: 41 MPa, Si: 27.4 MPa, Perovskite: 3.4 MPa	Numerical	Current study
Model B (Current Study)	ABAQUS	Indium: 41 MPa, Si: 27.4 MPa, GaN: 45.5 MPa	Numerical	Current study
Model C (Current Study)	ABAQUS	Indium: 44.5 MPa, Si: 28 MPa, GaAs: 48 MPa	Numerical	Current study

As aforementioned, all the models are exposed to the same temperature range, but the thermal stresses that occurred in each model are different, and the reason beyond that is the difference between the thermal expansion coefficient for each material, especially at the intersection zone. For instance, in model A, there is no high thermal stress or red zone in the intersection between the indium and perovskite, as the thermal expansion coefficient for the two materials is close. In contrast, high thermal stresses happened in the intersection zone between the indium and GaAs as a result of the large difference in the thermal expansion coefficients.

#### 4. Conclusion

The growth of renewable energy sources has become an essential focus recently, with solar energy presenting a viable alternative to conventional fossil fuels. Photovoltaic (PV) cells are essential for capturing solar energy and turning it into usable power, with the primary aim of solar cell research being the enhancement of the long-term energy production of PV systems. This study proposed three different models for MJPV cells in order to discover the thermal stress for perovskite, GaAs, and GaN upper layers with silicon as a substrate and indium spheres as middle junctions. The comparison of findings from the three models clearly indicates that models (A) and (B) exhibited much lower thermal stresses at all junctions compared to model (C), along with reduced deformation at the upper junction. This is likely due to the three models being constructed from distinct materials. The peak stress is observed in model (B), particularly within the GaAs upper layer. Overall, the three models have the potential to achieve higher efficiency than the other single-junction solar cells. Future research will investigate the impact of various thermal configurations in the context of solar panel operating conditions, aiming to mitigate dislocation propagation resulting from elevated thermal stresses via the Embedded Void Approach (EVA).

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